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BOX ISSUE FEE
PATENT
3531-0103P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Paul R. BERGER et al. Conf.: 8252
Appl. No.: 09/934,334 Group:
Filed: August 21, 2001 Examiner:
For: SI-BASED RESONANT INTERBAND TUNNELING
DIODES AND METHOD OF MAKING INTERBAND
TUNNELING DIODES

BATCH NO:

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner for Patents
Washington, DC 20231

December 4, 2001

Sir:

Pursuant to the USPTO Notice to File Corrected Application Papers dated October 4, 2001, attached hereto are five (5) sheets of corrected Formal Drawings. Please substitute these corrected drawings for the corresponding five (5) sheets of drawings on file in the above-identified application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee

Appl. No. 09/934,334

required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension
of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 

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ATTACHMENTS

(Rev. 11/09/01)